IRW

Docket No.: INTEL-021

<u>PATENT</u>

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Edward A. BURTON, Kumar

**ANSHUMALI** 

Assignee: INTEL CORPORATION

Group Art Unit: 2818

Confirmation No.: 7414

Serial No.: 10/607,550

Examiner: Long K. Tran

Filed: 6/27/2003

Customer No.: 34610

For: ADDITION OF METAL LAYERS WITH SIGNAL REALLOCATION TO A MICROPROCESSOR FOR INCREASED FREQUENCY AND LOWER POWER

## RESPONSE TO RESTRICTION REQUIREMENT

U.S. Patent and Trademark Office 220 20th St. South Customer Window, Mail Stop: Amendment Crystal Plaza Two, Lobby, Room 1B03 Arlington, VA 22202

Sir:

In response to the Restriction Requirement mailed on November 30, 2004 Applicant elects Group I (claims 1-14, 20 and 30) without traverse for further prosecution on the merits.

Applicants respectfully submit that this application is in condition for allowance. Favorable consideration and prompt allowance of the application is respectfully requested.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this,

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concurrent and future replies, including extension of time fees, to Deposit Account 16-0607 and please credit any excess fees to such deposit account.

Respectfully submitted, FLESHNER & KIM, LLP

ATTORNEYS FOR INTEL CORPORATION

Mark L. Fleshner Registration No. 34,596

Samuel W. Ntiros Registration No. 39,318

P.O. Box 221200 Chantilly, Virginia 20153-1200 703-766-3701 MLF/SWN:dac

Date: December 6, 2004

Please direct all correspondence to Customer Number 34610